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Amendments to the Specification:

Please replace the paragraph beginning on page 13 at line 19 with the following:

Fig 2B shows an exemplary embodiment of a typical base unit B. Base unit B includes conventional components, including an antenna 351 for receiving and transmitting data via RF, an RF down conversion circuit 353, an optional signal level detector 370 (e.g., a conventional received signal strength indicator (RSSI)), a decoder 356, BPSK and QPSK demodulators 362a, 362b which are selectable by switching means 361, a microcontroller 350, timing control circuit 355, memory 370, user interface 372, and power supply 374. For transmitting data, Base unit B further includes BPSK and QPSK modulators 366a, 366b which are selectable by switch means 365, PN encoder 320, an RF up conversion circuit 368 and adjustable gain RF output amplifier 369. These components are more fully described in U.S. Patent No. 5,950,124, which is fully incorporated herein by reference. It should be appreciated that BPSK and QPSK modulators/demodulators are shown only to illustrated the present invaction invention, and that other modulation/demodulation techniques are in common use, including BMOK and CCK.